

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	703/? .ccls. and (cache adj simulat\$3) and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:45
L2	0	711/? .ccls. and (cache adj simulat\$3) and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:45
L3	0	712/? .ccls. and (cache adj simulat\$3) and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:45
L4	0	714/? .ccls. and (cache adj simulat\$3) and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:45
L5	2	707/? .ccls. and (cache adj simulat\$3) and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:45
S1	59	(cache near3 simulat\$3).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 16:54
S2	23	S1 and (miss or hit same cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 16:41
S3	18	S2 and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 16:55
S4	3	S3 and segment	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 16:43

S5	89	(cache adj simulat\$3 or emulat\$3) same segment and (miss or hit same cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 10:39
S6	75	S5 and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 16:56
S7	36	S6 and (size same cache) and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/08 10:42
S8	1	(symbolic adj processing and circuit and matrix).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 18:37
S9	3	admittance adj matrix same symbolic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 18:38
S10	89	(cache adj simulat\$3 or emulat\$3) same segment and (miss or hit same cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 10:36
S11	75	S10 and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 10:40
S12	35	S11 and (size same cache) and buffer and performance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 10:36
S13	24	S12 and (performance same cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 10:37
S14	8	(cache adj simulat\$3 or emulat\$3) same performance same segment and (miss or hit same cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 10:40

S15	5	S14 and @ad<"20010413"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 10:40
S16	2	S15 and (size same cache) and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 13:36
S17	6	Lahiri.in. and cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 11:35
S18	3	nithrakashyap.in. and cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 11:37
S19	28	bridge.in. and cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 11:39
S20	0	S19 and (cache adj simulator same performance)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 11:41
S21	0	S19 and (cache adj simulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 11:41
S22	0	S19 and (cache same simulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 11:41
S23	1	cache adj simulator same performance same segment	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 17:38
S24	0	cache adj simulator same multi-segment	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 17:39